



ESOTERIC

Newsletter

Group of People with Special Knowledge.....



Academic Year 2018-19

Electronics and Communication Engineering Department, Hirasugar Institute of Technology, Nidasoshi

Volume 10

ABOUT DEPARTMENT

The Hirasugar Institute of Technology, Nidasoshi was established in the Year 1996 with Electronics & Communication Engineering Department as one among the four disciplines of Engineering to produce quality engineers in the field of Electronics and Communication under the divine spirit of his holiness Sriman Niranjan Pranava Swarupi Jagadguru Pancham Sri Shivalingeswar Maha Swamiji Siddha Sounsthan Math Nidasoshi, President of S. J. P. N. Trust. Over 140 students are being educated in the highly qualified learning atmosphere which has an investment of about 93.18 lakh rupees and for advanced tomorrow 11.03 lakh rupees investment has been planned to develop sophisticated learning environment.

The Department is having well experienced, qualified staff having good knowledge of both industry & academics. Staff members are attending various seminars & workshops regularly to upgrade themselves in different fields. The department is having ECSA (Electronics & Communication Students & Staff Association), which is an association of students and staff of the department. The sole purpose of this association is to bring out the hidden talents of our students in both curricular and extracurricular activities by organizing different department level activities.

The department has a library with about 737 well stacked books. Students counseling is being done and students' performance in I.A. test as well as attendance status will be intimated to their parents for their overall development.

BLESSINGS FROM MAHASWAMIJI



PRESIDENT
S.J.P.N. TRUST, NIDASOSHI

‘ಜಾಗತೀಕರಣ’ ಇಂದು ವಿಶ್ವದ ಬಹುದೊಡ್ಡ ಬೆಳವಣಿಗೆಯಾಗಿದೆ. ಹಾಗಾಗಿ ಇದರ ಗುರಿಗಂತವ್ಯಗಳನ್ನರಿತು ವಿದ್ಯಾರ್ಥಿಗಳು ಸಾಗಬೇಕಾಗಿದೆ. ಈ ಹಿನ್ನೆಲೆಯಲ್ಲಿ ನಮ್ಮ ಟ್ರಸ್ಟಿನ ಹಿರಾಶುಗರ ಇಂಜಿನೀಯರಿಂಗ್ ಮಹಾವಿದ್ಯಾಲಯದ ವಿದ್ಯಾರ್ಥಿಗಳು ಗುರಿಯನ್ನು ದಟ್ಟವಾಗಿಸಿಕೊಂಡು ಪ್ರತಿಭೆಗಳಲ್ಲಿ ಮೆಲುಗೈ ಸಾಧಿಸುತ್ತಿರುವುದು ಹೆಮ್ಮೆ ಎನಿಸಿದೆ. ವಿಶೇಷವಾಗಿ ವಿದ್ಯುನ್ಮಾನ ಮತ್ತು ಸಂಪರ್ಕ ವಿಭಾಗದ ವಿದ್ಯಾರ್ಥಿಗಳ ಸಾಧನೆ ಅಚ್ಚರಿ ಪಡುವಂತಹದೇ ಆಗಿದೆ. “ವಿದ್ಯಾರ್ಥಿಗಳ ಸಾಧನೆಯೇ ಸಂಸ್ಥೆಯ ಅಭಿವೃದ್ಧಿ” ಈ ನೋಟವಾಗಿ ಸ್ಮರಣ ಸಂಚಿಕೆ ಹೊರಬರುತ್ತಿರುವುದು ಸಂತೋಷದ ಸಂಗತಿ. ಮತ್ತಷ್ಟು ಮತ್ತಷ್ಟು ವಿದ್ಯಾರ್ಥಿಗಳು ಜ್ಞಾನ ಹೊಂದಿ "ವಿಶ್ವದ ನಿರ್ಮಾತ್ಮಗಳು" ಎಂಬ ಭಾಷೆಗೆ ಭಾಷ್ಯೆ ಬರೆಯಲಿ; ಈ ಬಗೆಯಲ್ಲಿ ಶ್ರಮಿಸುವ ಹೆಚ್ಚಿನ ಶಕ್ತಿಯನ್ನು ಶ್ರೀ ದುರದುಂಡೀಶ್ವರರು ಎಲ್ಲ ಗುರು ವರ್ಗಕ್ಕೆ ನೀಡಲೆಂದು ಆಶಿಸುತ್ತೇವೆ.

ಶ್ರೀಮನ್ ನಿರಂಜನ ಜಗದ್ಗುರು ಪಂಚಮ ಶ್ರೀ ಶಿವಲಿಂಗೇಶ್ವರ ಮಹಾಸ್ವಾಮೀಜಿ
ಸಿದ್ಧಸಂಸ್ಥಾನಮಠ, ನಿಡಸೋಸಿ ಮತ್ತು ಅಧ್ಯಕ್ಷರು, ಎಸ್.ಜೆ.ಪಿ.ಎನ್. ಟ್ರಸ್ಟ್

INSTITUTE VISION

“To be a preferred institution in Engineering Education by achieving excellence in teaching and research and to remain as a source of pride for its commitment to holistic development of individual and society”

INSTITUTE MISSION

“ To continuously strive for the overall development of students, educating them in a state of the art infrastructure, by retaining the best practices, people and inspire them to imbibe real time problem solving skills, leadership qualities, human values and societal commitments, so that they emerge as competent professionals”

DEPARTMENT VISION

“To be the centre of excellence in providing education in the field of Electronics and Communication Engineering to produce technically competent and socially responsible engineering graduates”

DEPARTMENT MISSION

“Educating students to prepare them for professional competencies in the broader areas of the Electronics and Communication Engineering field by inculcating analytical skills, research abilities and encouraging culture of continuous learning for solving real time problems using modern tools”

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

PEO 1:

Acquire core competence in Applied Science, Mathematics, and Electronics and Communication Engineering fundamentals to excel in professional carrier and higher study.

PEO 2:

Design, Demonstrate and Analyze the Electronic Systems which are useful to society.

PEO 3:

Maintain Professional and Ethical values, Employability skills, Multi disciplinary approach and an Ability to realize Engineering issues to broader social contest by engaging in lifelong learning.

PROGRAM SPECIFIC OUTCOMES (PSOs)

PSO 1:

An ability to understand the concepts of Basic Electronics and Communication Engineering and to apply them to various areas like Signal Processing, VLSI, Embedded Systems, Communication Systems and Digital & Analog Devices.

PSO 2:

An ability to solve complex Electronics and Communication Engineering problems, using latest hardware and software tools, along with analytical skills to arrive at cost effective and appropriate solutions.

FROM PRINCIPAL'S DESK

"I am delighted to know that department of Electronics and Communication of HSIT is coming out with its 10th volume of Department Newsletter "ESOTERIC". I hope and trust the Newsletter does the wonderful job of projecting accurately every worthy event and achievement of HSIT, Nidasoshi. It necessarily provides all the information that is awaited by stake holders.

I take this opportunity to assure all the students bright future and quality campus life. I also thank and congratulate the creative and enthusiastic editorial team for their sincere and dedicated efforts in bringing out this Newsletter."

Dr. S. C. Kamate
Principal

FROM HOD'S DESK

It is my privilege & pleasure to be the part of Electronics and Communication Department's 10th volume Newsletter "ESOTERIC". Our editorial board team has done wonderful job of projecting accurately every worthy event and achievements of ECE Dept., HSIT, Nidasoshi. It necessarily provides all the information that is awaited by everyone who cherishes HSIT.

My best wishes to all students of ECE department for their bright future. I thank and congratulate the creative and enthusiastic editorial team for their sincere and dedicated efforts in bringing out this Newsletter.

Dr. V. G. Kasabegoudar
HOD, ECE, Dept.

FACULTY MEMBERS

Name	Designation	Experience (in years)	Qualification
Dr. V. G. Kasabegoudar	HOD & Professor	23	Ph.D.
Dr. S. B. Shrigiri	Associate Professor	16	Ph.D.
Prof. M. M. Gadag	Assistant Professor	30	M.Tech. (Ph.D.)
Prof. S. S. Kamate	Assistant Professor	17	M. Tech. (Ph.D.)
Prof. S. B. Akkole	Assistant Professor	24	M. Tech. (Ph.D.)
Prof. S. S. Malaj	Assistant Professor	22	M. E.
Prof. S. S. Patil	Assistant Professor	15	M. Tech. (Ph.D.)
Prof. N. M. Patel	Assistant Professor	13	M. Tech.
Prof. D. M. Kumbhar	Assistant Professor	20	M. Tech.
Prof. D. B. Madihalli	Assistant Professor	12	M. Tech.
Prof. P. V. Patil	Assistant Professor	07	M. Tech.
Prof. S. S. Ittannavar	Assistant Professor	06	M. Tech. (Ph.D.)
Prof. B. P. Khot	Assistant Professor	03	M. Tech.

TECHNICAL SUPPORTING STAFF

Name	Designation	Experience	Qualification
Shri. P. S. Desai	Foreman	20	DEC
Shri. V. V. Guruvadeyar	Instructor	29	DEC
Shri. M. A. Attar	Instructor	11	DEC
Shri. M. B. Byali	Asst. Instructor	11	DEC

MINISTERIAL STAFF

Name	Designation	Experience	Qualification
Shri. B. D. Gurav	Helper	23	S. S. L. C.
Shri. C. S. Sooji	Helper	21	S. S. L. C.



IA TOPPERS IN THE YEAR 2017-18 (EVEN) AND 2018-19(ODD)

SEM: III			
Sl. No.	USN (2HN)	Name of the Student	Total Marks
01	17EC014	Nikhita Mane	331
02	17EC025	Shivani Kanerkar	327

SEM: IV			
Sl. No	USN (2HN)	Name of the Student	Total Marks
01	16EC013	Dalawai Laxmi	160
02	16EC010	Ashwini Shettimani	159
03	16EC042	Veenashri Boragalli	159

SEM: V			
Sl. NO.	USN (2HN)	Name of the Student	Total Marks
01	16EC036	Sunita Karabasannavar	160
02	16EC042	Veenashri Boragalli	160
03	16EC013	Dalawai Laxmi	158

SEM: VI			
Sl. No	USN (2HN)	Name of the Student	Total Marks
01	15EC040	Sulochana Belavi	159
02	15EC046	Udageri Sangamesh	159
03	15EC047	Vinod Jadhav	159
04	15EC028	Rekha Dharmatti	158

SEM: VII			
Sl. NO.	USN (2HN)	Name of the Student	Total Marks
01	15EC046	Udageri Sangamesh	239
02	15EC047	Vinod Jadhav	238

SEM: VIII			
Sl. No.	USN (2HN)	Name of the Student	Total Marks
01	14EC010	Ashwini Patil	249
02	14EC014	G. Alan	248

STUDENTS ACHIEVEMENT IN TECHNICAL PAPER PRESENTATION (A.Y: 2018-19)

Sl. No.	Title of the Paper	Author	Name of the Event	Venue	Date	Prize	Level
01	IOT Based Agriculture System Using Node MCU	Miss. Sunita Karabasannavar	INNOVATION-2K19	SITCE, YADRAV	28/02/2019	Participant	National
02	Design of High Speed Hardware Efficient Modified Booth Multiplier Using HDL.	Miss. Sulochana Belavi & Miss. Savita Dasangali	PRAXIS 2K19	KLECET, Chikodi	01/03/2019 & 02/03/2019	3 rd Prize	National
03	Fuel Cell	Miss. Dalawai Laxmi & Miss. Kavita Patil	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National

STUDENTS ACHIEVEMENT IN TECHNICAL PAPER PRESENTATION (A.Y: 2018-19)

Sl. No.	Title of the Paper	Author	Name of the Event	Venue	Date	Prize	Level
04	Design and Implementation of IOT Based EVM.	Mr. Sangamesh Udageri & Miss. Tejaswini Diggewadi	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
05	Implementation of Automatic College Bell Using Arduino	Miss. Veenashri Boragalli & Miss. Sunita Karabasannavar	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
06	Implementation of Data Encryption Standard Using Verilog.	Miss. Sana Attar & Miss. Amruta Hunashyali	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	2 nd Prize	National
07	Optical Fiber Expansion & 5G Correlations & Synergies	Miss. Laxmi Mannikeri & Miss. Sunita Hamidwade	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
08	Efficient FPGA Implementation of Address Generator for WiMAX Di interleaver	Miss. Akshata Nandaganvi & Miss. Gayatri. Gharabude	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
09	A Micro strip Patch Antenna Design at 28GHz for 5G Mobile Phone Applications	Miss. Seema Gani & Miss. Vidya Shetti	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National

STUDENTS ACHIEVEMENT IN TECHNICAL PAPER PRESENTATION (A.Y: 2018-19)

Sl. No	Title of the Paper	Author	Name of the Event	Venue	Date	Prize	Level
10	Smart Home Controlled By Alexa Using Raspberry Pi	Miss. Maheshwari Khatakalli & Miss. Rutuja. Patil,	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
11	Design of High Speed Hardware Efficient Modified Booth Multiplier Using HDL	Miss. Sulochana Belavi & Miss. Savita Dasangali	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
12	Design of Cache Memory with Cache Controller Using Verilog	Miss. Rekha Dharmatti & Miss. Amruta S M	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National
13	An Insight into Beyond CMOS Next Generation Computing using Quantum-dot Cellular Automata Nanotechnology	Miss. Aruna Chougala & Miss. Jayashree Madannavar	HIT QUEST 2019	HSIT, Nidasoshi	25/03/2019	Participant	National

STUDENTS ACHIEVEMENT IN TECHNICAL EVENTS (A.Y: 2018-19)

Sl. No.	Title	Name of the Student	Name of the Event	Venue	Date	Prize	State / National
01	PRAXIS 2K19	Miss. Sulochana Belavi & Miss. Savita Dasangali	Logic Mania	KLECET, Chikodi	01/03/2019 & 02/03/2019	Participant	National
02	PRAXIS 2K19	Miss. Akshata Nandganv & Miss. Gayatri Gharabude	Logic Mania	KLECET, Chikodi	01/03/2019 & 02/03/2019	Participant	National
03	HIT Quest 2019	Miss. Suvarna Sangave & Miss. Anuja Mane	Technical Quiz	HSIT, Nidasoshi	25/03/2019	Participant	National
04	HIT Quest 2019	Miss. Sunita Karabasannavar & Miss. Veenashri Boragalli	Technical Quiz	HSIT, Nidasoshi	25/03/2019	Participant	National
05	HIT Quest 2019	Miss. Vidya Sanadi & Miss. Pavitra Ranjanage	Technical Quiz	HSIT, Nidasoshi	25/03/2019	Participant	National

STUDENTS ACHIEVEMENT IN TECHNICAL EVENTS (A.Y: 2018-19)

Sl. No.	Title	Name of the Student	Name of the Event	Venue	Date	Prize	State / National
06	HIT QUEST 2019	Mr. Ramesh Huddhar & Mr. Vishal Hiramath	Technical Quiz	HSIT, Nidasoshi	25/03/2019	2 nd Prize	National
07	HIT QUEST 2019	Miss. Shraddha Hipparagi & Miss. Amruta Hunashyali	Technical Quiz	HSIT, Nidasoshi	25/03/2019	Participant	National
08	HIT QUEST 2019	Miss. Pratima Jugulkar & Miss. Akshata Shivannavar	Technical Quiz	HSIT, Nidasoshi	25/03/2019	Participant	National
09	HIT QUEST 2019	Mr. Vinit Kanthi & Mr. Vinayak Tupparatti	Technical Quiz	HSIT, Nidasoshi	25/03/2019	Participant	National
10	HIT QUEST 2019	Miss. Sunita Karabasannavar & Miss. Veenashri Boragalli	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
11	HIT QUEST 2019	Miss. Nisarga Bagalkot & Miss. Meenakshi Patil	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
12	HIT QUEST 2019	Miss. Suvarna Sangave & Miss. Komal Ranabhare	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
13	HIT QUEST 2019	Miss. Anuja Mane & Miss. Anuradha K	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
14	HIT QUEST 2019	Miss. Kaveri Daddimani & Mr. Deepak Karne	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
15	HIT QUEST 2019	Miss. Ashwini Shettimani & Miss. Aishwarya Patil	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
16	HIT QUEST 2019	Mr. Vinit Kanthi & Mr. Vinayak Tupparatti	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	1 st Prize	National
17	HIT QUEST 2019	Miss. Priyanka Patil & Miss. Veena Bomanale	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
18	HIT QUEST 2019	Miss. Pratima Jugulkar & Miss. Akshata Shivannavar	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National
19	HIT QUEST 2019	Miss. Sulochana Belavi & Miss. Savita Dasangali	Circuit Debugging	HSIT, Nidasoshi	25/03/2019	Participant	National

DETAILS OF INDUSTRIAL INTERNSHIP (A.Y: 2017-18)

Sl. No.	Name of the Student	Name of the Industry	Internship / Summer Training Period
01	Khatkalli Maheshwari	Technophilia, Sangli	10/07/2018 to 10/08/2018
	Patil Rutuja		
	Musale Shreya		
	Anuja T Mane		
	Chaitra Angadi		
02	Shweta Naik	Electrosal Hi Tech. Pvt. Ltd. Nipani	09/07/2018 to 09/08/2018
	Jyoti Chougala		
03	Anuradha Kareppagol	CITL Tech Versity, Bangalore, (KNOWX Innovations (p) Ltd. Bangalore)	09/07/2018 to 07/08/2018
	Arjevi Patil		
	Komal Ranbhare		
	Suvarna Sangave		
	Mustakim Pendari		
	Udageri Sangmesh		
04	Priyanka Patil	VI Solutions, Bangalore	09/07/2018 to 08/08/2018
	Rekha Dharmatti		
	Veena Bamanale		
	Swati Bogalkar		
	Priyanka Magadum		
	Jyoti Banahatti		
	Sankalp Patil		
	Vinod Jadhav		
	Balesh Nander		
	Vishal Hiremath		
	Sourabh Lohar		
	Shivkumar Pujari		
05	Shivangi Singh	Tech Fortune Technologies, Bangalore	11/07/2018 to 10/08/2018
	Shweta Parulekar		
	Vrushali Mali		
	Padmaja Shetti		
	Laxmi Mannikeri		
	Rani Jogur		
	Aruna Chougala		

DETAILS OF INDUSTRIAL INTERNSHIP (A.Y: 2017-18)

Sl. No.	Name of the Student	Name of the Industry	Internship / Summer Training Period
06	Akshata B. Nandaganvi	APSYS Solutions, Bangalore.	09/07/2018 to 09/08/2018
	Akshata Jamakhandi		
	Amrutha S. M.		
	Bhavani Shiralkar		
	Gayatri Gharabude		
	Jagadeesh Managanvi		
	Jayashri Madannavar		
	Kushal Baragi		
	Lalita Ambalazeri		
	Mohan A Bassapuri		
	Pooja Patil		
	Praveen B Potaraddi		
	Raghavendra L M		
	Ramesh Huddar		
	Sammed M Hulikoppe		
	Saniya Rafik Jagadal		
	Savita Dasangali		
	Shraddha Katti		
Sneha M. Magadum			
Sulochana B Belavi			
Manjunath B Alagundi			
Varada Prakash Joshi			
07	Miss. Tejashiwini A. Diggewadi	SKADA Technology Solution Pvt. Ltd., Pune	09/07/2018 to 09/08/2018

DETAILS OF INDUSTRIAL VISITS

Sl. No.	Name of the Industry Visited	No. of Students	Semester	Place	Date
01	Kaynes Technology	50	IV & VI	Mysore	17/04/2019
02	DITAP-V Automation Pvt. Ltd.	32	VIII	Pune	19/04/2019

SPORTS / OTHER COMPETITIONS

Sl. No.	Name of the Student	Name of the Event	Venue	Date	Prize	National/State/ Inter College Level
01	Miss. Tejashwini Diggewadi	Rangoli Competition VTU Youth Festival Janani-2018	Bheemanna Khandre Institute of Technology, Bhalki.	01/11/2018	1 st Prize	VTU Inter Collegiate
02	Miss. Sunita Karabasannavar	QUIZ Competition VTU Youth Festival janani-2018	Bheemanna Khandre Institute of Technology, Bhalki.	01/11/2018	Participated	VTU Inter Collegiate
03	Miss. Aishwarya Patil	Dance Competition VTU Youth Festival janani-2018	Bheemanna Khandre Institute of Technology, Bhalki.	01/11/2018	Participated	VTU Inter Collegiate
04	Mr. Mustakim Pendari	Cricket	SDM, Dharwad.	27/03/2019	Participated	VTU Inter Collegiate
05	Mr. Praveen Potaraddi	Cricket	SDM, Dharwad.	27/03/2019	Participated	VTU Inter Collegiate
06	Mr. Sangamesh Kodekalmath	Cricket	SDM, Dharwad.	27/03/2019	Participated	VTU Inter Collegiate

DEPARTMENT ACTIVITIES

Sl. No.	Topic of the Seminar or Event	Date of Conduct	Resource Person and Address
01	Industry Expert Lecture on Industrial Automation & IOT	04/09/18	Sri. Shitalkumar Borganve, HCL Technologies Ltd, Bangalore.
02	Industry Expert Lecture on Opportunities in VLSI Domain	06/10/18	Sri. Rajashekar Karikatti, Sanklap Semiconductor Pvt. Ltd. Hubli.
03	Three day's workshop on IOT Based Node MCU	22/02/2019 to 24/02/2019	Sri. Shitalkumar Borganve, HCL Technologies Ltd, Bangalore.
04	Three day's workshop on PCB Design & Testing	01/03/2019 to 03/03/2019	Mr. Tulshidas Salunke Electrosal Hi Tec Pvt.Ltd. Shripewadi Nipani.

FACULTY DEVELOPMENT INITIATIVES

Sl. No	Name of the Workshop/ Short term Course Attended	Organized by	Date	Name of the Faculty
01	One week FDP on Student Induction on Universal Human Values	BVB CET, Hubballi & AICTE Bangalore	12/07/2018 to 18/07/2018	Prof. S. S. Ittannavar
02	5- Days STTP on Algorithms and Architectures for High Efficient Video Processing Systems (AAHEVPS-2018)	NITK SURATHKAL	20/08/2018 to 24/08/2018	Prof. D. B. Madihalli
03	One week STTP on "IoT & DSP Applications Design.	KLEMSS CET Belagavi	01/01/2019 to 06/01/2019	Prof. S. S. Kamate
04	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Dr. V. G. Kasabegoudar
05	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Dr. S. B. Shrigiri
06	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. M. M. Gadag
07	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. S. S. Kamate
08	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. S. B. Akkole
09	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. S. S. Malaj
10	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. D. M. Kumbhar
11	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. S. S. Patil
12	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. N. M. Patel
13	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. D. B. Madihalli
14	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. P. V. Patil
15	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. S. S. Ittannavar
16	One week FDP on "Internet of Things and Its Real Time Applications".	HSIT, Nidasoshi	28/01/2019 to 02/02/2019	Prof. B. P. Khot

RESEARCH PAPERS PUBLISHED IN JOURNALS / CONFERENCE BY FACULTIES

Sl.No.	Name of the Faculty	Publication details
01	Dr. V. G. Kasabegoudar & Prof. S.B. Akkole	S. B. Akkole and V. G. Kasabegoudar, "Design and Optimization of E-shape Multiband Micro strip Patch Antenna Using Fractal Geometry for Wireless Communication," <i>International Conference on Advanced Scientific Innovation in Science Engineering and Technology (ICASISET-2019)</i> , April 2019.
02	Prof. S. S. Kamate	Akshata Hirekodi, Rakesh Patil and S. S. Kamate, "A 4-bit , 3.2 GSPS flash analog to digital converter with a new multiplexer based encoder," <i>International Journal of Emerging Technologies and Innovative Research</i> , vol. 5, issue 8, pp. 728 – 733, August 2018.
03	Prof. S. S. Patil	N. M. Patel, S. S. Patil, M. A. Navi and N. B. Kanwade, "Design of pipelined RISC MIPS processor (8 - Bit) using VLSI technology," <i>International Journal of Engineering Research and Application</i> , vol. 8, issue 9 (Part II), pp.01-05, September 2018.
04	Prof. N. M. Patel	N. M. Patel, S. S. Patil, M. A. Navi, and N. B. Kanwade, "Design of pipelined RISC MIPS processor (8 - Bit) using VLSI technology," <i>International Journal of Engineering Research and Application</i> , vol. 8, issue 9 (Part II), pp. 01-05, September 2018.
		N. M. Patel , P. V. Patil , G. M. Alan, P. B. Done, "Design of pipelined RISC MIPS processor (16 - Bit) using VLSI technology," <i>International Journal of Emerging Technologies and Innovative Research</i> , vol.5, issue 9, pp. 143-152, September 2018.
05	Prof. D. B. Madihalli, Prof. D. M. Kumbhar, Prof. P. V. Patil & Prof. S. S. Ittannavar	D. B. Madihalli, D. M. Kumbhar, P. V. Patil and S. S. Ittannavar "Agricultural Robot," <i>International Journal of Emerging Technologies and Innovative Research</i> vol.5, issue 9 , pp. 292–294, September 2018.
06	Prof. P. V. Patil	A. R. Patil , P. V. Patil , Rajeshwari Khot, Laxmi Patil and Anusha Fernandes "Implementation of data encryption standard algorithm using verilog," <i>International Journal of Emerging Technologies and Innovative Research</i> , vol.5, issue 8 , pp.734–746, August 2018.
		N. M. Patel , P. V. Patil , G. M. Alan and P. B. Done, "Design of pipelined RISC MIPS processor (16 - Bit) using VLSI technology," <i>International Journal of Emerging Technologies and Innovative Research</i> , vol.5, issue 9, pp. 143-152, September 2018.
07	Prof. D. B. Madihalli & Aruna Chougala	Prof. D. B. Madihalli and Aruna Chougala, "Advanced Home Automation System using Raspberry Pi" <i>International Journal of Emerging Technologies and Innovative Research</i> , vol.6, issue 6, pp. 982-985, June 2019.
08	Prof. B. P. Khot & Prof. S. S. Ittannavar	B. P. Khot and S. S. Ittannavar "Accident Prevention and Security System for Vehicles Using Multi Sensors," <i>International Journal of Emerging Technologies and Innovative Research</i> vol.6, issue 6, page no.413-416, June 2019.

VGST SPONSORED PROJECTS

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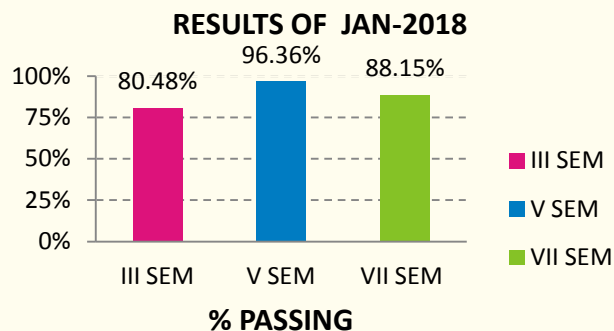
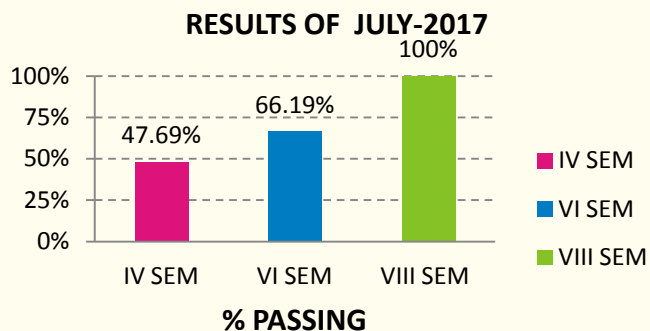
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04	Accident Prevention & Security System for Vehicles using Multi Sensors	Prof. B. P. Khot	7000/-	KSCST, Bangalore	2018-19

FACULTY VISITED AS A RESOURCE PERSON

Sl. No.	Name of the Faculty	Topic of the Seminar	Held at	Date
01	Prof. N. M. Patel	Jury Member for Paper Presentation.	S. J. P. N Trust, BCA Nidasoshi	26/09/2018
02	Prof. S. S. Patil	Seminar on Digital Logic and Computer Design	S. J. P. N Trust, BCA Nidasoshi	28/02/2019
03	Prof. N. M. Patel	Seminar on Non Verbal Communication	S. J. P. N Trust, BCA Nidasoshi	09/08/2018
04	Prof. S. S. Patil	Jury Member for Circuit Debugging and Flip for PRAXIS-2K19.	KLECET, Chikodi	02/03/2019
05	Prof. S. B. Akkole	Jury Member for Final Year Project Exhibition-2019	S. J. P. N Trust, Polytechnic Nidasoshi	15/03/2019

ACADEMIC EXCELLENCE



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Arjevi Patil
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2. Wipro, Bangalore.



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PalC Networks, Bangalore.



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Rani Jogur
Tech- Fortune Technologies Bangalore.



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