




CURRICULUM VITAE

Name:	Mrs. Sujata S. Kamate	
Designation:	Asst. Prof.	
Department:	ECE Department	
Specialization & Research Interests:	VLSI Design, Digital Electronics and Data converters	
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Address:	Hirasugar Institute of Technology, Nidasoshi-591236. Dist : Belgaum, Karnataka.	

2. ACADEMIC QUALIFICATION (in reverse Chronological order):

Degree	Year	University/Board
Pursuing Ph. D	2017	VTU, Belagavi
M. Tech	2012	GIT, Belagavi
B.E	2002	HIT, Nidasoshi

3. ANY OTHER QUALIFICATION: --

4. PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
HIT Nidasoshi	Assistant Professor	01-04-2018 to Till date
HIT Nidasoshi	Associate Professor	01-01-2017 to 31-03-2018
HIT Nidasoshi	Assistant Professor	01-10-2013 to 31-12-2016
HIT Nidasoshi	Lecturer	06-09-2004 to 31-01-2008
C.B. Kore polytechnic, Chikodi	Lecturer	13-11-2002 to 03-09-2004

5. ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
Women cell and Internal committee convener	October 2018 to till date	Organizing awareness events and competitions for students.
IEEE Branch counselor	June 2014 to till date	Conducting events, with students and reporting to IEEE Bangalore section for section reimbursement
Dept. NBA criteria -3 coordinator	March 2017 to till date	Preparing complete criteria -3 file of the ECE Dept.
NAAC criteria -3 member	March 2018 to till date	Preparing NAAC criteria -3 file of the ECE Dept.
Dept. NBA coordinator	October 2013 to March 2017	Preparing documents for NBA as per the instructions of higher authority.

6. COURSES TAUGHT:

- Signals and Systems
- Engineering Electromagnetics
- Microwaves and Antennas
- VLSI Design
- Computer Organization
- Engineering Statistics and Linear Algebra
- Logic Design
- Microelectronic Circuits
- Control Systems
- Analog and Mixed mode VLSI
- Analog Communication
- Antenna and wave propagation
- Basic Electronics

- Transmission lines and waveguides
- Optical fiber Communication
- *Universal Human Values*

7. RESEARCH SUPERVISION:

A. Ph.D:

- i. **Awarded** : Not yet
- ii. **Submitted** : Not yet
- iii. **Ongoing** : yes (VLSI Design)

B: M.Tech.

Awarded : 2012 (Digital Electronics)

C. BE/B. Tech Degree Oriented (Bachelor Level Dissertation):

Awarded : 2002 (Electronics and Communication)
Submitted : 25 (3 KSCST selected and 2 for Shrusti)
Ongoing : 02

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

- As a Branch counselor of **IEEE HIT SB**, successfully organized the events under SB, submitted the yearly reports to IEEE Bangalore section in a prescribed format for section rebate and got totally **Rs. 55, 665/-** sanctioned for the years 2015, 2016 and 2017. Successfully submitted the report for the year 2019.
- Convener for Women Empowerment cell HIT, Nidasoshi.
- Convener for ICC/ Antisexual Harrashment Committee HIT, Nidasoshi.
- Convener for **Women's Day Celebration functions** for all the years
- Organized "**Awareness on women safety and sexual harassment**" on 08/03/2019 on behalf of Anti-sexual Harassment committee.
- Organized talk on "**Awareness on Legitimate Provisions for Women**" on 30th October 2018.
- Successfully organized inaugural function of HIT- Quest 2018.
- Successfully organized inaugural function of Technovision- 20 in February-2020.
- Successfully organized inaugural function of Technovision- 2019.
- Successfully organized inaugural function of HIT- Quest 2019.
- Worked in registration committee for icriset-2019.
- Department Coordinator ICRASET -2016.
- Convener of Registration committee for ICRASET- 2018.
- Worked as member in various for Annual Day celebration in HIT- **Sambhrama**.
- Department Coordinator for the HIT- Quest -2013.

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

ISTE: Life Member of Indian Society for Technical Education (LMISTE)

10. PUBLICATIONS:**A. BOOKS/MONOGRAPHS:**

1. Authored: Nil.

2. Edited: Nil

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS:

- i. Kamate, SS (Kamate, Sujata S.); Rajani, HP (Rajani, H. P.) ; Mallaraddi, V (Mallaraddi, Vidyavati) "Efficient Design Techniques of Flash ADC for High Speed and Ultra Low Power Applications" Published in Journal of Bioscience Biotechnology Research Communications, Volume: 13, Issue: 13, Pages: 144-149. December 2020. **(E-SCI indexed)** DOI: 10.21786/bbrc/13.13/20,
- ii. Kamate, SS (Kamate, Sujata S.); Rajani, HP (Rajani, H. P.) ; Mallaraddi, V (Mallaraddi, Vidyavati) Designing A Novel Architecture to Reduce Stand by and Dynamic Power Dissipation for Sleepy Keeper CMOS Logic Circuits Published in Journal of Bioscience Biotechnology Research Communications, Volume: 13, Issue: 13, Pages: 144-149 DOI: 10.21786/bbrc/13.13/20, 2020 **(E-SCI indexed)**.
- iii. SS Kamate, SS Malaj, "Design and Implementation of IoT based Electronic Voting Machine", a journal of composition theory, Volume 12 , Issue VI, Pages: 100-105 June 2019(ISSN : 0731-6755).
- iv. Akshata Hirekodi Rakesh Patil S S Kamate, (2018). A 4-bit, 3.2GSPS Flash ADC with a new multiplexer based encoder: Journal of Emerging Technologies and Innovative Research, 5(8), 728-733.
- v. S.S. Malaj, S.S. Kamate "RFID & GSM Based Toll Tax System", International Journal of Scientific and Research Publications, Volume 7, Issue 1, Pages: 110-14, IJSRP, 2017.
- vi. S. S. Kamate, Akshata Naikar, S. S. Malaj, (2016). Design and Implementation of low power CMOS Analog to Digital Converter in cadence tool: Journal of Advances in Science and Technology, Vol. 12, Issue No. 25, (Special Issue), 239-242.

C. PAPERS IN CONFERENCES PROCEEDINGS:

i. S. S. Kamate, H. P. Rajani and V. Mallaraddi, "Design and Implementation of Low Power Flash Analog to Digital Converter," *2023 Second International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT)*, Trichirappalli, India, 2023, pp. 1-6, doi:10.1109/ICEEICT56924.2023.10157537.

ii). V. Mallaraddi, H. P. Rajani and S. S. Kamate, "Static and Dynamic power optimization using Leakage Feedback approach for nanoscale CMOS VLSI circuits," *2022 First International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT)*, 2022, pp. 1-5, doi: 10.1109/ICEEICT53079.2022.9768527.

iii. S. S. Kamate, Dr. H. P. Rajani, (2020). Design and Implementation of 3-bit Flash Analog to Digital Converter for Low power and High speed Applications with new Ex-OR based ROM Encoder, *Second International Conference on Advances in Electrical and Computer Technologies 2020*, 12 - 13, June 2020 Coimbatore, Tamil Nadu, India.

iv. S. S. Kamate, Dr. H. P. Rajani, (2018). Emerging Design techniques for low power and high speed CMOS Flash ADC, *Int. Conf. on Recent Innovations in Electrical, Electronics & Communication Engg.*, 27-28 July 2018, Bhubaneshwar.

v. Akshata Hirekodi Rakesh Patil S S Kamate, (2018). A 4-bit, 3.2GSPS Flash ADC with a new multiplexer based encoder: *Journal of Emerging Technologies and Innovative Research*, 5(8), 728-733.

vi. S. S. Kamate, Akshata Naikar, S. S. Malaj, (2016). Design and Implementation of low power CMOS Analog to Digital Converter in cadence tool: *Journal of Advances in Science and Technology*, Vol. 12, Issue No. 25, (Special Issue), 239-242.

11. Patents/Copyrights/IPR (If Any)

12. INVITED TALKS:

13. RESEARCH PROJECTS (COMPLETED/ONGOING):

14. PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/ WORKSHOPS/ CONFERENCES:

Sl. No.	Topic	Period	Date	Venue
1.	Software Engineering and Testing Methodologies	4 Days	16 th to 19 th March 2021	HIT, Nidasoshi
2.	FDP on "Introduction to Microchip FPGA"	5 Days	19th to 23rd July 2021	Bapuji Institute of Engineering and Technology,
3.	Battery Management & Control Techniques in Electric Vehicles	6 days	14 th -19 th Dec 2020	HIT, Nidasoshi
4.	FDP on "VLSI Design Tools, Techniques and Applications", by E&ICT Academy NIT, Warangal.	8 Days	10 th to 17 th August 2020	Vishnu Institute of Technology, Bhimavaram, AP
5.	International level online FDP on "Advanced Research Trends in image Processing"	3 days	14-16 th July 2020	HIT, Nidasoshi
6.	online FDP on "Skill Enhancement for	2 Days	8 th to 9 th June 2020	JCE Belagavi

	Academicians			
7.	National Level One-Week FDP on "Machine Learning Based Project Architecture Design", under TEQIP-III.	5 Days	15th to 19th June 2020.	B.V.V.Sangha's Basaveshwar Engineering College (A), Bagalkot
8.	Four days workshop on "Design and Simulation of Microstrip Antenna using HFSS Tool"	4 Days	26 th to 29 th February 2020	HIT, Nidasoshi
9.	VGST sanctioned FDP on "Machine Learning and Data Analytics"	5 Days	08 th January to 11 th January 2020	HIT, Nidasoshi
10.	Workshop on "Internet of Things and its real time applications organized by E&ICT Academy NIT, Warangal.	6 Days	28 th to 02 th Feb 2019	HIT, Nidasoshi
11.	STTP on IoT and DSP Applications Design	6 Days	01st January to 06 th January 2019	KLE MSS CET, Belagavi
13.	STTP on Artificial Intelligence and Medical Image processing	7 Days	16 th to 22 nd January 2018	KLE MSS CET, Belagavi
14.	Workshop on "Research Methodology"	1 Day	11 th November 2017	GIT, Belagavi
15.	Workshop on "NBA SAR Filling and preparedness for Assessment"	3 Days	15 th to 17 th September, 2017	HIT, Nidasoshi
16.	Two week ISTE STTP on "CMOS Mixed Signal and RF VLSI Design"	6 Days	26 th December, 2016 to 04 th February 2017.	GIT, Belagavi
17.	Workshop on Micro-nano- Photonics	1 Day	30 th January, 2015	Jain College of Engg. and Technology, Belagavi.
18.	Workshop on "Advanced VLSI Design using Cadence Design Suite"	3 Days	16 th to 18 th July 2014	JSSATE, Bagalore
19.	Workshop on "Latest Trends in EDA for Digital Electronics and VLSI Technology"	2 Days	22nd-23 rd Sept. 2013	MMEC, Belgaum
20.	Three days Workshop on Signals & Systems	3 Days	4 th - 6 th October, 2012	HIT, Nidasoshi
21.	Workshop on "Application of Mathematics in Engineering".	2 Days	30 th - 31 st August, 2010	HIT, Nidasoshi
22.	Workshop on "Hands on Workshop on VLSI Design Using Cadence Tools site"	3 Days	28th-30th July, 2010	Sambhram Institute of Technology, Bangalore
23.	Workshop on "CMOS VLSI and ASIC designs using MICROWIND tool"	2 Days	27th - 28 th August 2009	SIT, Tumkur
24.	Workshop on "CMOS VLSI and ASIC designs using MICROWIND"	2 Days	31 st August, to 01st Sept., 2007	BMS Bangalore
25.	STTP on "Role of Women in Technical Education"	5 Days	2nd - 6th August 2004	AIT, Bangalore

26	One day workshop on "Embedded System Development using ARM Controller".	1 Day	19th June 2004	ISM, Bangalore
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Conferences:

1. Second International Conference on Advances in Electrical and Computer Technologies 2020, 12 - 13, June 2020 Coimbatore, Tamil Nadu, India.
2. International Conference on Recent Advances in Science and Technology (ICRISET-2019)
3. International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering 27-28 July 2018, Bhubaneshwar.
4. International Conference on Recent Advances in Science and Technology (ICRISET-2018)
5. International conference on smart electrical Systems ICSES-2016, Belagavi.

Workshops:

1. Organized Workshop on Basic Arduino programming to department prefinal year students from 17th to 19th January 2023.
2. Organized Workshop on Basic Arduino programming & it's applications to Dept. prefinal year students from 16th to 18th December 2021.

Organized Webinars on

1. **Professional Communication Skills for all ECE students on 02.09.2020 from 2.30 to 4.00pm by JayPrakash Rau, IRS.**
2. **Ethics in Engineering Profession for all ECE students on 02.09.2020 from 2.30 to 4.00pm by JayPrakash Rau, IRS**

Attended Webinars on

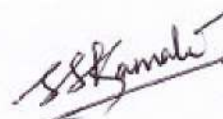
- "HOW TO CRACK GATE IN FIRST ATTEMPT" organized by Electronics and Communication Engineering Department in Association with ACE Engineering Academy, Hyderabad on 28th June 2020.
- **Career after Covid-19**" organized by Electronics and Communication Engineering Department on 21st June 2020.

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

Nil

16. ANY OTHER SIGNIFICANT INFORMATION:

- Achieved 100% results in many subjects.
- Always achieved very good results, in critical courses like Engineering Electromagnetics and Signals and Systems.



(Prof. S. S. Kamate)